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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Sreenivasan, Sidlgata V
App. No.: 10/805,916 GPAU: 2123
Filing Date: March 22, 2004 Examiner:
Dkt. No.: PA133-09D13 Conf. No.: 2031
For: Method of Determining Alignment of a Template and a
Substrate Having a Liquid Disposed Therebetween

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

The PTO did not receive the following
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Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other:

to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

For the convenience of the Examiner, the references cited in the parent patent application number 09/907,512 are notated in attached forms PTO/SB/08A with bold font. Copies of these references are not required nor provided.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

CERTIFICATE OF TRANSMISSION


I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents.

Signed: Katrina Prati

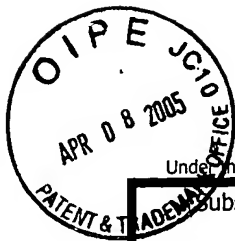
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Date: 4-6-05

Respectfully Submitted,



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PTO/SB/08A (08-03)

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT***(use as many sheets as necessary)*

Sheet 1 of 8

Complete if Known

APPLICATION NUMBER	10/805,916
FILING DATE	03/22/2004
FIRST NAMED INVENTOR	Sreenivasan et al.
Group Art Unit	2123
Examiner Name	Unassigned
Attorney Docket Number	PA133-09D13

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office 3	Number ⁴	Kind Code ⁵ (if known)				
	B1		2002/0098426	A1	Sreenivasan et al.	7/25/2002		
	B2		4,731,155		Napoli et al.	3/15/1988		
	B3		5,414,514		Smith et al.	5/9/1995		
	B4		5,425,848		Haisma et al.	6/20/1995		
	B5		5,772,905		Chou	6/30/1998		
	B6		5,808,742		Everett et al.	9/15/1998		
	B7		6,088,103		Everett et al.	7/11/2000		
	B8		6,309,580	B1	Chou	10/30/2001		
	B9		6,334,960	B1	Willson et al.	1/1/2002		
	B10		6,391,217	B2	Schaffer et al.	5/21/2002		
	B11		6,522,411	B1	Moon et al.	2/18/2003		
	B12		6,696,220	B2	Bailey et al.	2/24/2004		
	B13		2002/0177319	A1	Chou	11/28/2002		
	B14		6,713,238	B1	Chou et al.	3/30/2004		
	B15		2004/0008334	A1	Sreenivasan et al.	1/15/2004		
	B16		2004/0009673	A1	Sreenivasan et al.	1/15/2004		
	B17		2004/0124566	A1	Sreenivasan et al.	7/1/2004		
	B18		3,783,520		King	1/8/1974		

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	B19	6,150,231		Muller et al.	11/21/2000	
	B20	6,489,068	B1	Kye	12/3/2002	
	B21	6,646,662	B1	Nebashi et al.	11/11/2003	
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	B35	6,828,244	B2	Chou	12/7/2004	
	B36	6,809,356	B2	Chou	10/26/2004	

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	B37	5,737,064		Inoue et al.	4/7/1998	
	B38	2004/0197843	A1	Chou et al.	10/7/2004	
	B39	2004/0156108	A1	Chou et al.	8/12/2004	
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	B53	10/895,214		Voison	7/20/2004	
	B54	10/923,629		Sreenivasan et al.	8/20/2004	

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	B55	10/978,285		Willson et al.	10/29/2004	
	B56	11/000,321		Nimmakayala et al.	11/30/2004	
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	B63	6,517,995	B1	Jacobenson et al.	2/11/2003	
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	B68	5,849,222		Jen et al.	12/15/1998	

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		Office 3	Number ⁴	Kind Code ⁵ (if known)				

	B69	WO	00/21689		Chou et al.	4/20/2000		
	B70	JP	02-92603		Kurikawa et al.	4/3/1990		
	B71	JP	02-24848		Kamio	1/26/1990		
	B72	WO	01/47003		Steiner et al.	6/28/2001		
	B73	WO	02/07199		Chou	1/24/2002		
	B74	WO	2004/114016		Chou et al.	12/29/2004		
	B75	WO	99/05724		Chou et al.	2/4/1999		
	B76	WO	03/010289		Chou et al.	2/6/2003		
	B77	WO	03/079416		Chou	9/25/2003		
	B78	WO	03/099536		Chou et al.	12/4/2003		

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Sheet 6 of 8				Attorney Docket Number	PA133-09D13

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	B79	Chou, Nanoimprint Lithography and Lithographically Induced Self-Assembly, MRS Bulletin, pp. 512 - 517, 7/1/2001	
	B80	Choi et al., Design of Orientation Stages for Step and Flash Imprint Lithography, Precision Engineering, pp. 192 - 199, 1/1/2001	
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	B87	Chou et al., Lithographically Induced Self-assembly of Periodic Polymer Micropillar Arrays, J. Vac. Sci. Technol. B 17 (6), pp. 3197 - 3202, 11/1/1999	

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	B88	Colburn et al., Step and Flash Imprint Lithography: A New Approach to High-Resolution Patterning, Proc. Of SPIE, vol. 3676, 3/1/1999	
	B89	Heidari, Nanoimprint Lithography at the 6 in. Wafer Scale, J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000	
	B90	Translation of Japanese Patent 02-92603	
	B91	Translation of Japanese Patent 02-24848	
	B92	Chou et al., Ultrafast and Direct Imprint of Nanostructures in Silicon, Nature, Col. 417, (June 2002), pp. 835-837, 6/1/2002	
	B93	Moon et al., Interferometric-Spatial-Phase Imaging for Six-Axis Mask Control, MIT NanoStructures Laboratory, Research Laboratory of Electronics, 10/17/2003	
	B94	Chou et al., Nanoimprint Lithography, Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996	
	B95	Colburn et al., Development and Advantages of Step-and-Flash Lithography, Solid State Technology, 7/1/2001	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				APPLICATION NUMBER	10/805,916
				FILING DATE	03/22/2004
				FIRST NAMED INVENTOR	Sreenivasan et al.
				Group Art Unit	2123
				Examiner Name	Unassigned
Sheet	8	of	8	Attorney Docket Number	PA133-09D13

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	B97	Bailey et al., Step and Flash Imprint Lithography: Defect Analysis, Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001	
	B98	Bailey et al., Step and Flash Imprint Lithography: Template Surface Treatment and Defect Analysis, Journal of Vacuum Science, B 18(6), pp. 3572-3577, 11/1/2000	
	B99	Wronosky et al., Wafer and Reticle Positioning System for the Extreme Ultraviolet Lithography Engineering Test Stand, Emerging Lithography Technologies, Proceedings of SPIE Vol. 3997, pp. 829-839, 7/1/2000	
	B100	Schneider et al., Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers	

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